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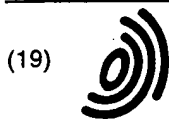
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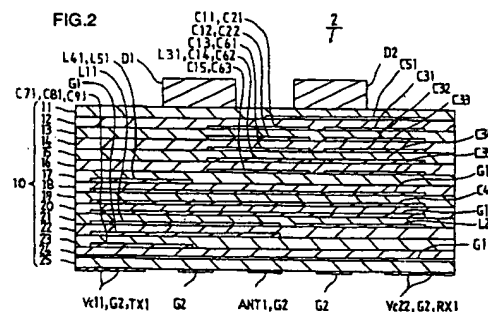
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(54) Composite high frequency apparatus and method for forming same

(57) A composite high frequency apparatus includes a high frequency filter (F1) and a high frequency switch (1) which have a substantially reduced size and do not require an impedance matching circuit. The apparatus includes a multilayered base (10) having an outer surface with a plurality of diodes (D1/D2), an external ground electrode (G2), an external electrode (TX) for a transmission circuit, an external electrode (RX) for a receiving circuit, an external electrode (ANT1) for an antenna circuit and external electrodes (V_{C11}, V_{C22}) for control terminals located thereon. A plurality of strip lines, capacitor electrodes and an external grounding electrode are located within the multilayered base (10).



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Description

BACKGROUND OF THE PRIOR ART

The present invention relates to a composite high frequency apparatus, and more specifically, to a composite high frequency apparatus including a high frequency device, for example, a high frequency switch, and a filter and to a method for forming a composite high frequency apparatus by simultaneously forming an inter-connected apparatus including at least one high frequency device and at least one filter.

A high frequency switch shown in Fig. 10 is used for switching connections between a transmission circuit TX and an antenna ANT and between a receiving circuit RX and an antenna ANT via signal lines in a portable digital telephone set, for example.

An example of a high frequency device, such as a high frequency switch, has a configuration shown in the circuit diagram of Fig. 11. The high frequency switch 1 is connected to an antenna ANT, a transmission circuit TX and a receiving circuit RX by signal lines V1. A high frequency component, such as a diode D1, has an anode that is connected via a capacitor C1 to the transmission circuit TX. The anode of the diode D1 is also connected to ground via a series circuit including a strip line L1 forming part of a first transmission line and a capacitor C4. The length of the strip line L1 is substantially equal to or less than $\lambda/4$, where the wavelength of a signal sent from the transmission circuit TX is λ . A control terminal Vc1 is connected to an intermediate point between the strip line L1 and the capacitor C4. A control circuit (not shown) for switching the high frequency switch 1 is connected to the control terminal Vc1. A series circuit including a strip line L3 forming part of the first transmission line and a capacitor C6 is connected to both ends of the diode D1 (between the anode and the cathode). The cathode of the diode D1 is connected to the antenna ANT via a capacitor C2.

The receiving circuit RX is connected to the capacitor C2, which is connected to the antenna ANT, via a series circuit including a strip line L2 forming part of the first transmission line and a capacitor C3. As in the case of the strip line L1, the length of the strip line L2 is substantially equal to or less than $\lambda/4$. Another high frequency component, for example, a diode D2, has an anode connected to an intermediate point between the strip line L2 and the capacitor C3. The cathode of the diode D2 is connected to ground via a capacitor C5. Further, a control terminal Vc2 is connected to an intermediate point between the diode D2 and the capacitor C5. A control circuit (not shown) for switching the high frequency switch 1 is connected to the control terminal Vc2 to thereby complete the circuit of the high frequency switch 1.

When a signal is transmitted through the high frequency switch 1, a positive bias voltage is applied to the control terminal Vc1 while a negative bias voltage is applied to the control terminal Vc2. As these voltages work

as bias voltages in the forward direction to bias the diodes D1 and D2, the diodes D1 and D2 will be turned ON. At this time, direct currents will be cut by the capacitors C1 to C6 and the voltages applied to the control terminals Vc1 and Vc2 will be applied only on a circuit including the diodes D1 and D2. Thus, the strip line L2 will be grounded by the diode D2 and will resonate at a transmission frequency and impedance will be made almost infinite. Consequently, a signal sent from the transmission circuit TX will be transmitted to the antenna ANT through the capacitor C1, the diode D1 and the capacitor C2 without being transmitted to the side of the receiving circuit RX in most cases. Further, because the strip line L1 is grounded via the capacitor C4, the strip line L1 will resonate at a transmission frequency and impedance will be made almost infinite. As a result, leakage of a transmitted signal to the ground side will be prevented.

On the other hand, at the time of receiving a signal, a negative bias voltage will be applied to the control terminal Vc1 while a positive bias voltage will be applied to the control terminal Vc2. As these voltages work as bias voltages in reverse directions relative to the diodes D1 and D2, the diodes D1 and D2 will be turned OFF, and thereby a signal received from the antenna ANT will be transmitted to the receiving circuit RX through the capacitor C2, the strip line L2 and the capacitor C3 without being transmitted to the transmission circuit TX in most cases.

In this way, the high frequency switch 1 allows switching of transmitted and received signals by controlling bias voltages to be applied to the control terminals Vc1 and Vc2.

Further, the series circuit including the strip line L3 and the capacitor C6 are used for increasing impedance on a connection point with the strip line L3 when the diode D1 is OFF and for reducing insertion loss and reflection loss by forming a parallel resonance circuit to be resonated by synthetic capacitance between the capacitor C6 and the diode D1 which is OFF and the inductance component of the strip line L3, and by resonating at its resonance frequency which is substantially the same as a frequency of a received signal.

Besides the circuit construction of a high frequency switch described above, there are various types of high frequency switches available. For example, a high frequency switch as described in Japanese Laid-Open Patent No. 6-197042 and Japanese Laid-Open Patent No. 6-197043 and a high frequency switch having a circuit configuration such as the one shown in Japanese Laid-Open Patent No. 7-74762 can also be used.

Further, other high frequency components such as transistors, FETs, and any other suitable device can be used instead of the high frequency components, such as the diodes D1 and D2 described above. In addition, instead of the strip lines L1, L2 and L3, other transmission lines such as coplanar lines and any other suitable components can be used.

Conventionally, however, problems occur when high

frequency devices, such as high frequency switches, are used to form a high frequency apparatus by connecting filters to the high frequency devices. The high frequency devices and filters are designed and manufactured differently and independently and are then connected to each other. As a result, the combined high frequency device and filter occupy relatively large areas on a printed circuit board and make the circuit arrangement thereof more complicated. In addition, impedance matching between the high frequency device and the filter must be done to connect the high frequency device and the filter. Therefore, an impedance matching circuit must be designed specifically for the particular high frequency device and filter to be connected to each other. Once the impedance matching filter is designed, the impedance matching filter must be manufactured and connected to the high frequency device and the filter. Consequently, the cost, time and difficulty of manufacturing an apparatus having an interconnected high frequency device and filter with necessary impedance matching circuit is substantially increased.

SUMMARY OF THE INVENTION

The preferred embodiments of the present invention solve the problems with the conventional apparatus having an interconnected high frequency device and filter. One object of at least one of the preferred embodiments of the present invention is to provide a composite high frequency apparatus having a substantially reduced size for occupying a reduced amount of space on a printed circuit board. Another object of at least one of the preferred embodiments of the present invention is to simplify the circuit arrangement of a composite high frequency apparatus while eliminating the need for an impedance matching circuit.

In order to solve the problems of the conventional devices described above, the composite high frequency apparatus and method of forming the apparatus according to at least one of the preferred embodiments of the present invention includes a high frequency device mounted on an outer surface of a multilayered base which includes a laminated structure of a plurality of dielectric layers and a signal line connected to a ground electrode, wherein the high frequency device is located at least on the outer surface of the multilayered base or the dielectric layer, a first transmission line connecting the signal line and the ground electrode and a filter connected to the high frequency component via a second transmission line and signal lines located in the multilayered unit.

Further, a diode is preferably used as a high frequency component, a strip line is preferably used as the transmission line and a high frequency switch is preferably used as the high frequency device. A low-pass filter can be used as the filter. Alternatively, all other suitable high frequency components, transmission lines, high frequency devices and filters can also be used as will be

understood from the following description of the preferred embodiments of the present invention.

By locating the high frequency device and the filter on one multilayered base including a plurality of dielectric layers and thereby creating a composite high frequency apparatus, the overall dimension of the apparatus is reduced as compared to a conventional device wherein a high frequency device and a filter are separately made and then connected to each other. Also, by combining the circuit of the high frequency device and the circuit of the filter and simultaneously forming the high frequency device and the filter, desired impedance matching between the circuit of the high frequency device and the circuit of the filter is achieved without the need for a separate impedance matching circuit.

Other features and advantages of the present invention will become apparent from the following description of the invention which refers to the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a diagram showing a circuit of a composite high frequency apparatus according to a preferred embodiment of the present invention;

Fig. 2 is a sectional view showing the composite high frequency apparatus shown in Fig. 1;

Fig. 3 is an exploded perspective view showing the composite high frequency apparatus of Fig. 1;

Fig. 4 is a view showing a circuit construction of a composite high frequency apparatus according to another preferred embodiment of the present invention;

Fig. 5 is a view showing a circuit construction of a composite high frequency apparatus according to another preferred embodiment of the present invention;

Fig. 6 is a view showing a circuit construction of a composite high frequency apparatus according to another preferred embodiment of the present invention;

Fig. 7 is a view showing a circuit construction of a composite high frequency apparatus according to another preferred embodiment of the present invention;

Fig. 8 is a view showing a circuit construction of a composite high frequency apparatus according to another preferred embodiment of the present invention;

Fig. 9 is a view showing a circuit construction of a composite high frequency apparatus according to another preferred embodiment of the present invention;

Fig. 10 is a view showing a circuit construction of a conventional high frequency device; and

Fig. 11 is a diagram showing the circuit of the conventional high frequency device shown in Fig. 10.

DETAILED DESCRIPTION OF PREFERRED EMBODIMENTS

Preferred embodiments of the present invention will be described with reference to the accompanying drawings. Description of elements similar to or the same as the conventional elements shown in Figs. 10 and 11, will be omitted to avoid redundancy. Such similar elements contained in the preferred embodiments of the present invention will be identified by the same reference numerals and characters used to describe the conventional components.

Fig. 1 is a diagram showing a circuit of a composite high frequency apparatus 2 according to a preferred embodiment of the present invention. In the preferred embodiment shown in Fig. 1, a filter F1 may preferably be a low-pass filter such as a Butterworth type low-pass filter or any other suitable filter.

The filter F1 is preferably connected between a transmission circuit TX and one end of a capacitor C1 of a high frequency device 1. The filter F1 preferably includes strip lines L4 and L5 which form a second transmission line. The filter F1 also preferably includes capacitors C7, C8 and C9. Description of the connections relating to the filter F1 is omitted because such connections are known.

Fig. 2 is a sectional view showing the composite high frequency apparatus 2 of Fig. 1. The high frequency apparatus 2 includes a multilayered base 10. As shown in the preferred embodiment of Fig. 3, the multilayered base 10 is preferably formed by laminating dielectric layers 11 to 25 of the first to fifteenth dielectric layers successively from top to bottom. Diodes D1 and D2 are preferably mounted on the first dielectric layer 11. Capacitor electrodes C51, C11, C21, C31, C12, C22, C32, C13, C33, C61, C15, C35, C63, C41, C71, C81 and C91 are preferably located on the layers 12 to 15 of the second to the fifth dielectric layers and the layers 17, 20 and 24 of the seventh, tenth and fourteenth dielectric layers.

Capacitor electrodes C14, C34 and C62 and a strip line electrode L31 are preferably formed on the sixth dielectric layer 16. Strip line electrodes L41, L51, L11, and L21 are preferably formed on the eighth and twelfth dielectric layers 18 and 22. Ground electrodes G1 are preferably formed on the ninth, eleventh, thirteenth and fifteenth dielectric layers 19, 21, 23 and 25.

Further, an external electrode for a transmission circuit TX1, an external electrode for a receiving circuit RX1, an external electrode for an antenna ANT1, external control electrodes Vc11 and Vc22 and external ground electrodes G2 are preferably formed on a lower surface of the fifteenth dielectric layer 25 (25u in Fig. 3). By forming signal line electrodes (not shown in Fig. 3), at desired locations on the dielectric layers 11 to 25 and by forming external electrodes (not shown in Fig. 3) on the outer surface of the multilayer base 10 and electrically connecting the external electrodes, the composite high frequency apparatus 2 is constructed to have the

circuit configuration shown in Fig. 1.

More particularly, capacitor C1 of the high frequency switch 1 comprises capacitor electrodes C11 to C15, capacitor C2 comprises capacitor electrodes C21 and C22, capacitor C3 comprises capacitor electrodes C31 to C35, capacitor C4 comprises capacitor electrode C41, capacitor C5 comprises capacitor electrode C51, and capacitor C6 comprises capacitor electrodes C61 to C63. Capacitors C7, C8 and C9 of the filter circuit F1 comprise capacitor electrodes C71, C81 and C91, respectively.

Coil patterns L1 to L3 of the high frequency switch 1 comprise strip line electrodes L11 to L31, respectively. Coil patterns L4 and L5 of the filter circuit F1 comprise strip line electrodes L41 and L51, respectively.

When manufacturing such a composite high frequency apparatus 2, dielectric ceramic green sheets are preferably prepared. On the dielectric ceramic green sheets, electrode paste is preferably printed in accordance with the forms of the respective electrodes and signal lines. Then, by laminating the dielectric ceramic green sheets with electrode paste printed thereon and baking the ceramic green sheets, a multilayered base having laminated dielectric layers will be formed. Further, by printing the electrode paste on the outer surface of the multilayered base and baking the electrode paste, external electrodes will be formed. It is also possible to form the composite high frequency apparatus by printing electrode paste to form external electrodes and baking the electrodes integrally after the dielectric green sheets are laminated.

The composite high frequency apparatus including a high frequency device and a filter formed on one multilayered base by laminating a plurality of dielectric layers has an overall physical dimension that is substantially less than the conventional devices in which a high frequency device and a filter are separately formed and then connected. The composite high frequency apparatus according to at least one of the preferred embodiments of the present invention achieves a reduced volume and occupies less area on a printed circuit board. Also, by combining the circuit of the high frequency device and the circuit of the filter and simultaneously designing and forming the circuits of the high frequency device and the filter, impedance matching between the circuit of the high frequency device and the circuit of the filter is achieved. Consequently, it is not necessary to design, manufacture and connect an impedance matching circuit between the circuit of the high frequency device and the circuit of the filter. Therefore, the manufacturing and assembly of the circuit of the high frequency device and the circuit of the filter is easier and less expensive than the conventional methods. Further, because the impedance matching circuit is not necessary, the composite high frequency device will be formed in a substantially shorter period of time because the time required for designing and forming the impedance matching circuit is unnecessary.

Further, in the description of the preferred embodi-

ment of Fig. 1, reference was made to the capacitors and strip lines formed on the multilayered base. However, resistance components like chip resistors, printed resistors, and other suitable resistance components and electronic components may be formed on the surface of the multilayered base or may be formed within the multilayered base.

In the preferred embodiment of Fig. 1, the high frequency device and the filter are connected such that the filter F1 is connected between the transmission circuit TX and the high frequency switch 1. However, it is possible to obtain the same effect by selectively connecting the filter F1 between the transmission circuit TX, the receiving circuit RX or the antenna ANT and the high frequency switch 1.

Several alternative preferred embodiments of the present invention will be described in the following paragraphs in relation to Figs. 4-9.

The filter F1 may preferably be connected between the antenna ANT and the high frequency switch 1 as shown in Fig. 4.

The filter F1 may preferably be connected between the receiving circuit RX and the high frequency switch 1 as shown in Fig. 5.

The filter F1 may preferably be connected between the transmission circuit TX and the high frequency switch 1 and between the antenna ANT and the high frequency switch 1 as shown in Fig. 6.

The filter F1 may preferably be connected between the transmission circuit TX and the high frequency switch 1 and between the receiving circuit RX and the high frequency switch 1 as shown in Fig. 7.

The filter F1 may preferably be connected between the receiving circuit RX and the high frequency switch 1 and between the antenna ANT and the high frequency switch 1 as shown in Fig. 8.

The filter F1 may preferably be connected between the transmission circuit TX and the high frequency switch 1, between the receiving circuit RX and the high frequency switch 1 and between the antenna ANT and the high frequency switch 1 as shown in Fig. 9.

Further, in another preferred embodiment of the present invention, a filter, for example, a low-pass filter, may be connected to the high frequency device. However, instead of a low-pass filter, a high-pass filter, a band-pass filter, a band elimination filter and any other suitable filter may be used to be combined with or connected to the high frequency device.

The effect of formation according to at least one of the preferred embodiments of the present invention will be described in detail with respect to an example where a low-pass filter is used.

A composite high frequency apparatus may be constructed by forming a high frequency device such as a high frequency switch including a multilayered base of laminated dielectric layers having the dimensions of about 6.3 x 5.0 x 3.0 mm and a low-pass filter having the dimensions of about 4.5 x 3.2 x 2.0 mm on one multilay-

ered base. As a result, the entire dimensions of the apparatus is about 6.3 x 5.0 x 3.0 mm which are about the same as the entire dimensions of a conventional high frequency switch. The volumes and occupied areas on a printed circuit board of the composite high frequency apparatus produced by at least one preferred embodiment of the present invention are substantially reduced. Further, by combining the circuit of the high frequency device and the circuit of the filter and simultaneously designing and forming the circuits, no impedance matching circuit is needed as with a conventional high frequency device and low-pass filter.

Thus, according to at least one of the preferred embodiments of the present invention, by creating a composite high frequency apparatus by forming a high frequency device and a filter on one multilayered base made of a plurality of dielectric layers, an overall dimension of the apparatus is substantially less than the overall dimension of the conventional high frequency device and filter which are independently designed and formed and then connected. The composite high frequency apparatus according to at least one of the preferred embodiments of the present invention has reduced volume and occupies substantially less area on a printed circuit board. Further, by combining the circuit of the filter and the circuit of the high frequency device and simultaneously designing and forming the circuits, it is possible to achieve impedance matching between the two circuits. Accordingly, it is not necessary to add an impedance matching circuit and the circuit construction of the preferred embodiments is simplified. In addition, the period of time required for designing, forming and connecting the impedance matching circuit as required in the prior art devices is eliminated because an impedance matching circuit is not necessary in the preferred embodiments of the present invention.

Although the present invention has been described in relation to particular preferred embodiments thereof, many other variations and modifications and other uses will become apparent to those skilled in the art. It is preferred, therefore, that the present invention be limited not by the specific disclosure herein, but only by the appended claims.

Claims

1. A composite high frequency apparatus comprising: a single multilayered unit (2) including at least one high frequency device (1) and a high frequency filter (F1) being electrically connected to each other within said single multilayered unit.
2. The composite high frequency apparatus of claim 1, wherein the at least one high frequency device (1) comprises a high frequency switch.
3. The composite high frequency apparatus of claim 1,

- wherein the high frequency filter (F1) comprises at least one of a low-pass filter, a band-pass filter, a band elimination filter and a high-pass filter.
4. The composite high frequency apparatus of claim 1, wherein the single multilayered unit (2) includes a high frequency component (D1/D2) mounted on an outer surface of the multilayered base (10). 5
 5. The composite high frequency apparatus of claim 4, wherein the high frequency component comprises at least one of a diode (D1/D2), a transistor and a FET. 10
 6. The composite high frequency apparatus of claim 1, wherein the single multilayered unit (2) comprises a plurality of laminated dielectric layers (11-25). 15
 7. The composite high frequency apparatus of claim 6, wherein the high frequency device (1) is located on at least one of the outer surfaces of one of the dielectric layers of the single multilayered unit (2). 20
 8. The composite high frequency apparatus of claim 5, wherein the single multilayered unit (2) further comprises a signal line connected to a ground electrode, a first transmission line (L1/L11) connected between the signal line and the ground electrode. 25
 9. The composite high frequency apparatus of claim 1, wherein the high frequency filter (F1) is connected with a high frequency switch (1) by a second transmission line (L2/L21) and signal lines located in the single multilayered unit (2). 30
 10. The composite high frequency apparatus of claim 9, wherein the single multilayered unit (2) further comprises a signal line connected to a ground electrode, the second transmission line is connected to the high frequency device by the signal line. 35 40
 11. A method of forming a composite high frequency apparatus, the method comprising steps of:
forming a plurality of dielectric layers (11-25) including a plurality of electronic components formed thereon;
joining the plurality of dielectric layers to connect the plurality of electronic components and to form a single multilayered unit (2) including at least one high frequency device (1) and a high frequency filter (F1) being electrically connected to each other within said single multilayered unit. 45 50
 12. The method of claim 11, wherein the step of forming the plurality of dielectric layers (11-25) includes the step of forming a plurality of dielectric ceramic green sheets. 55
 13. The method of claim 11, wherein the step of forming the plurality of dielectric layers (11-25) includes the step of forming a plurality of electronic components by printing electrode paste on the plurality of dielectric layers.
 14. The method of claim 13, further comprising the steps of laminating the dielectric layers having the electronic components printed thereon and baking the dielectric layers and electronic components.
 15. The method of claim 14, further comprising the step of forming external electrodes on at least one exterior surface (25u) of the single multilayer unit (2) and baking the external electrodes.
 16. A method of forming a composite high frequency apparatus, the method comprising the step of:
simultaneously forming a high frequency device (1) and a high frequency filter (F1) in a single unit (2), wherein the high frequency device and the high frequency filter are electrically connected to each other and an impedance of the high frequency device (1) is substantially equal to an impedance of the high frequency filter (F1).
 17. The method of claim 16, wherein said high frequency device (1) comprises a high frequency switch.
 18. The method of claim 16, wherein said high frequency filter (F1) comprises one of a low-pass filter, a band-pass filter, a band elimination filter and a high-pass filter.
 19. The method of claim 16, wherein the step of simultaneously forming the high frequency device (1) and the high frequency filter (F1) includes the step of forming a single multilayered unit (2) including a plurality of dielectric layers (11-25) having a plurality of electronic components formed thereon.
 20. The method of claim 19, further comprising the step of joining the plurality of dielectric layers (11-25) to connect the plurality of electronic components.

FIG.1

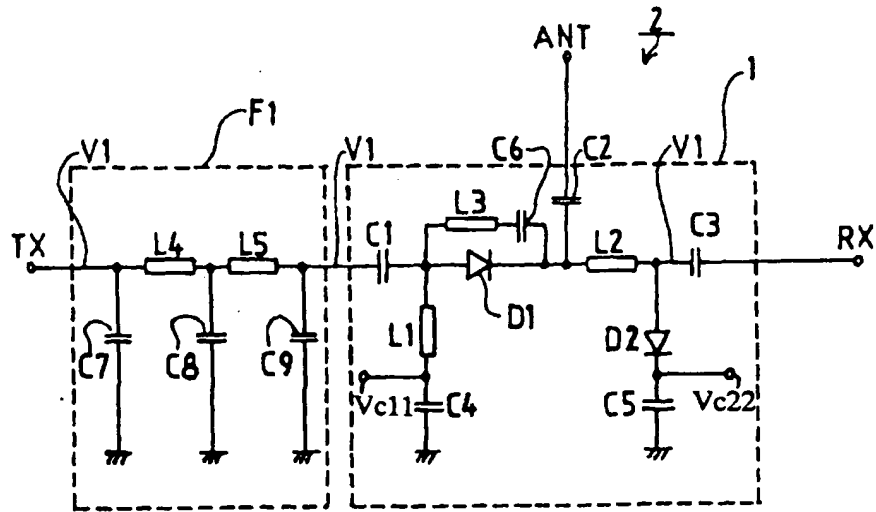


FIG.2

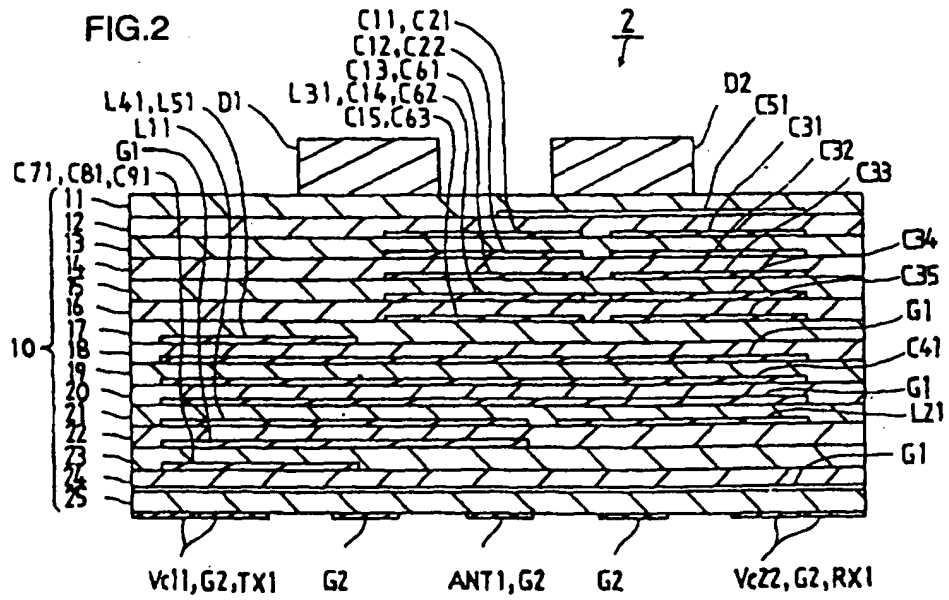


FIG.3

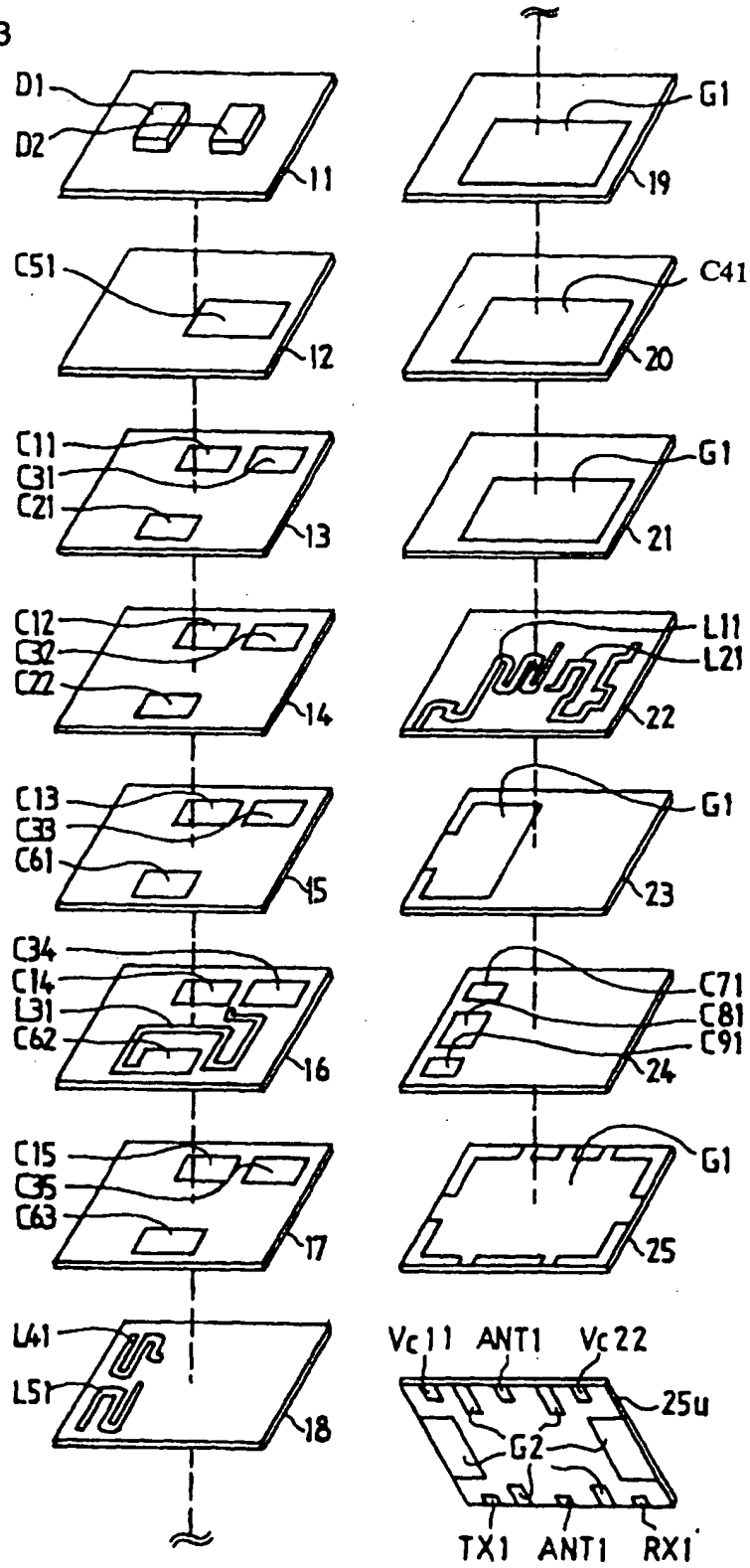


FIG.4

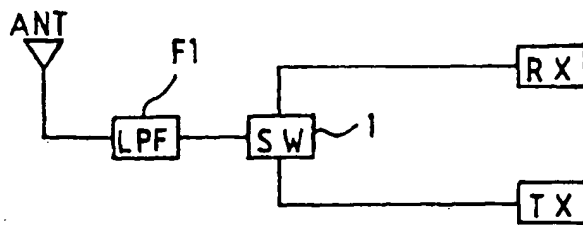


FIG.5

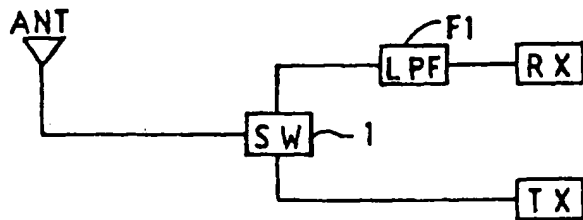


FIG.6

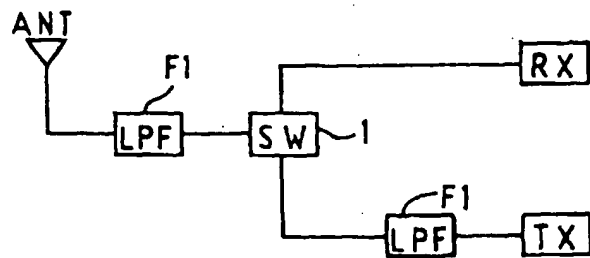


FIG.7

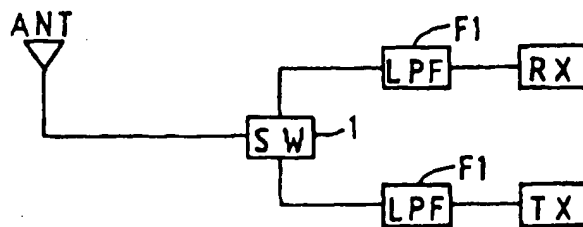


FIG.8

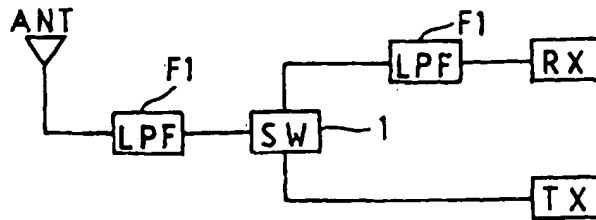


FIG.9

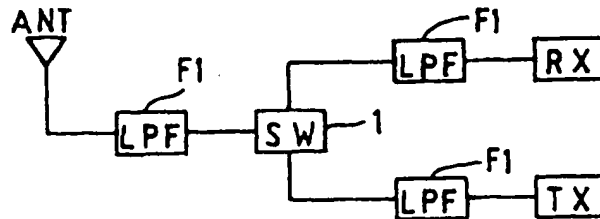


FIG.10
PRIOR ART

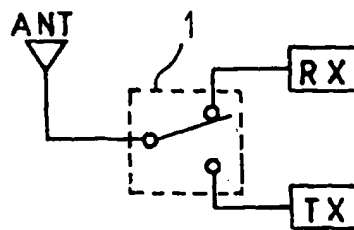
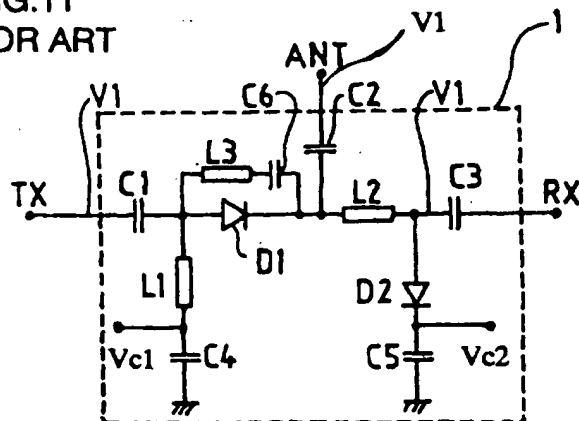


FIG.11
PRIOR ART





European Patent
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EUROPEAN SEARCH REPORT

Application Number
EP 95 40 2177

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
X	IEICE TRANSACTIONS ON ELECTRONICS, vol. E76-c, no. 6, June 1993 TOKYO JP, pages 912-918, XP 000389645 Y. TAGUCHI ET AL. 'Microwave characteristics of alumina-glass composite multi-layer substrates with co-fired copper conductors' * page 913, left column, line 7 - page 914, left column, line 2 * * page 917, right column, line 21 - line 25; figures 1,2 *	1-7,9, 11-20	H01P3/08
X	EP-A-0 468 801 (NGK INSULATORS LTD) * page 2, line 49 - page 5, line 53; figure 1 *	1,4,6,7, 11-15	
X	US-A-4 899 118 (POLINSKI SR.) * column 4, line 24 - column 5, line 51; figures 1,2 *	1,4-7, 11-15	
A	US-A-4 353 132 (SAITOH ET AL.) * column 3, line 64 - column 5, line 29; figures 4-6 *	1	
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 3 January 1996	Examiner Den Otter, A
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